Amendments to the Claims

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims

1. (Currently Amended) An image processing apparatus comprising:

a reception unit adapted to receive at least three encoded image data;

a decoding unit adapted to decode one of the encoded image data to generate a main frame;

a sub frame generation unit adapted to extract low frequency component from each one of the other encoded image data without fully decoding the other encoded image data, and generate sub frames using the low frequency components extracted from the other encoded image data; and

an image signal generation unit adapted to combine the main frame and the generated sub frames, and generate an image signal including the main frame ~~and~~ combined with the generated sub frames.

2. (Previously Presented) An apparatus according to claim 1, wherein the reception unit receives the at least three encoded image data through a serial bus.

3. (Previously Presented) An apparatus according to claim 2, wherein the serial bus is based on the IEEE 1394-1995 standard.

4. (Previously Presented) An apparatus according to claim 1, wherein the reception unit is a digital interface based on the IEEE 1394-1995 standard.

5. (Previously Presented) An apparatus according to claim 1, further comprising:

a switch unit adapted to switch the encoded image data corresponding to the main frame and the encoded image data corresponding to one of the sub frames, in response to an operation of a predetermined operation key.

6. (Previously Presented) An apparatus according to claim 1, further comprising:

a recording unit adapted to record the encoded image data corresponding to the main frame on a storage medium, in response to an operation of a predetermined operation key.

7. (Previously Presented) An apparatus according to claim 1, wherein the at least three encoded image data are based on the SD format of the DV standard.

8. (Currently Amended) An image processing method comprising ~~steps of~~:

receiving at least three encoded image data;

decoding one of the encoded image data to generate a main frame;

extracting low frequency component from each one of the other encoded image data without fully decoding the other encoded image data;

generating sub frames using the low frequency components extracted from the other encoded image data; ~~and~~

combining the main frame and the generated sub frames; and

generating an image signal including the main frame combined with ~~and~~ the generated sub frames.

9. (Currently Amended) A method according to claim 8, wherein ~~the reception step receives~~ the at least three encoded image data are received through a serial bus.

10. (Previously Presented) A method according to claim 9, wherein the serial bus is based on the IEEE 1394-1995 standard.

11. (Previously Presented) A method according to claim 8, wherein the at least three encoded image data is received through a digital interface based on the IEEE 1394-1995 standard.

12. (Currently Amended) A method according to claim 8, further comprising ~~a step of~~:

switching the encoded image data corresponding to the main frame and the encoded image data corresponding to one of the sub frames, in response to an operation of a predetermined operation key.

13. (Currently Amended) A method according to claim 8, further comprising ~~a step of~~:

recording the encoded image data corresponding to the main frame on a storage medium, in response to an operation of a predetermined operation key.

14. (Previously Presented) A method according to claim 8, wherein the at least three encoded image data are based on the SD format of the DV standard.